PAT-NO:

JP358006138A

DOCUMENT-IDENTIFIER: JP 58006138 A

TITLE:

FLATTENING METHOD FOR PHOSPHORUS SILICATE

**GLASS FILM** 

PUBN-DATE:

January 13, 1983

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N/A

APPL-NO:

JP56104160

APPL-DATE:

July 2, 1981

INT-CL (IPC): H01L021/316; H01L029/78

US-CL-CURRENT: 148/DIG.133, 257/E21.271, 438/784, 438/FOR.400,

438/FOR.493

#### ABSTRACT:

PURPOSE: To solve problems regarding the disconnection of Al wiring, the increase of diffusion length or the reduction of the phosphorus concentration of the PSG by flowing the PSG in steam gas exceeding atmospheric pressure.

CONSTITUTION: A LOCOS oxide film 2, a gate oxide film 3 and a polycrystal

5/19/05, EAST Version: 2.0.1.4

silicon gate layer 4 with approximately 6,000Å film thickness are formed onto a P type silicon substrate 1. The arsenic ions of the quantity of injection of 5×10<SP>15</SP>/cm<SP>2</SP> are injected at voltage such as

the acceleration voltage of 160KeV, and diffusion layers 5 functioning as drain and source regions are coated. The PSG 6 of the phosphorus concentration of 8mol% is further coated. The PSG 6 is flowed in a high pressure oeve for 10min

at 900°C in the steam of the gas pressure of 8kg/cm<SP>2</SP>. Lastly, the

PSG 6 is flowed in high-pressure steam, and the Al wiring 7 is formed, thus completing a MOS type transistor.

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# PATENT ABSTRACTS OF JAPAN

(11)Publication number:

58-006138

(43) Date of publication of application: 13.01.1983

(51)Int.CI.

H01L 21/316

(21)Application number : 56-104160

H01L 29/78

(22) Date of filing:

02.07.1981

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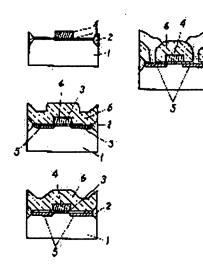
KAMEI ICHIZO

# (54) FLATTENING METHOD FOR PHOSPHORUS SILICATE GLASS FILM

## (57)Abstract:

PURPOSE: To solve problems regarding the disconnection of Al wiring the increase of diffusion length or the reduction of the phosphorus concentration of the PSG by flowing the PSG in steam gas exceeding atmospheric pressure.

CONSTITUTION: A LOCOS oxide film 2, a gate oxide film 3 and a polycrystal silicon gate layer 4 with approximately 6,000Å film thickness are formed onto a P type silicon substrate 1. The arsenic ions of the quantity of injection of 5×1015/cm2 are injected at voltage such as the acceleration voltage of 160KeV, and diffusion layers 5 functioning as drain and source regions are coated. The PSG 6 of the phosphorus concentration of 8mol% is further coated. The PSG 6 is flowed in a high pressure oeve for 10min at 900°C in the steam of the gas pressure of 8kg/cm2. Lastly, the PSG 6 is flowed in high-pressure steam, and the Al wiring 7 is formed, thus completing a MOS type transistor.



### **LEGAL STATUS**

[Date of request for examination]

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[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]